CM200DY-24TH

HIGH POWER SWITCHING USE
INSULATED TYPE

Collector current I_C .... **200** A

Collector-emitter voltage V_{CES} .... **1200** V

Maximum junction temperature T_{vjmax} .... **175** °C

- ●dual switch (half-bridge)
- ●Copper base plate (Nickel-plating)
- ●Tin-plating tab terminals
- ●RoHS Directive compliant
- ●UL Recognized under UL1557, File No. E323585

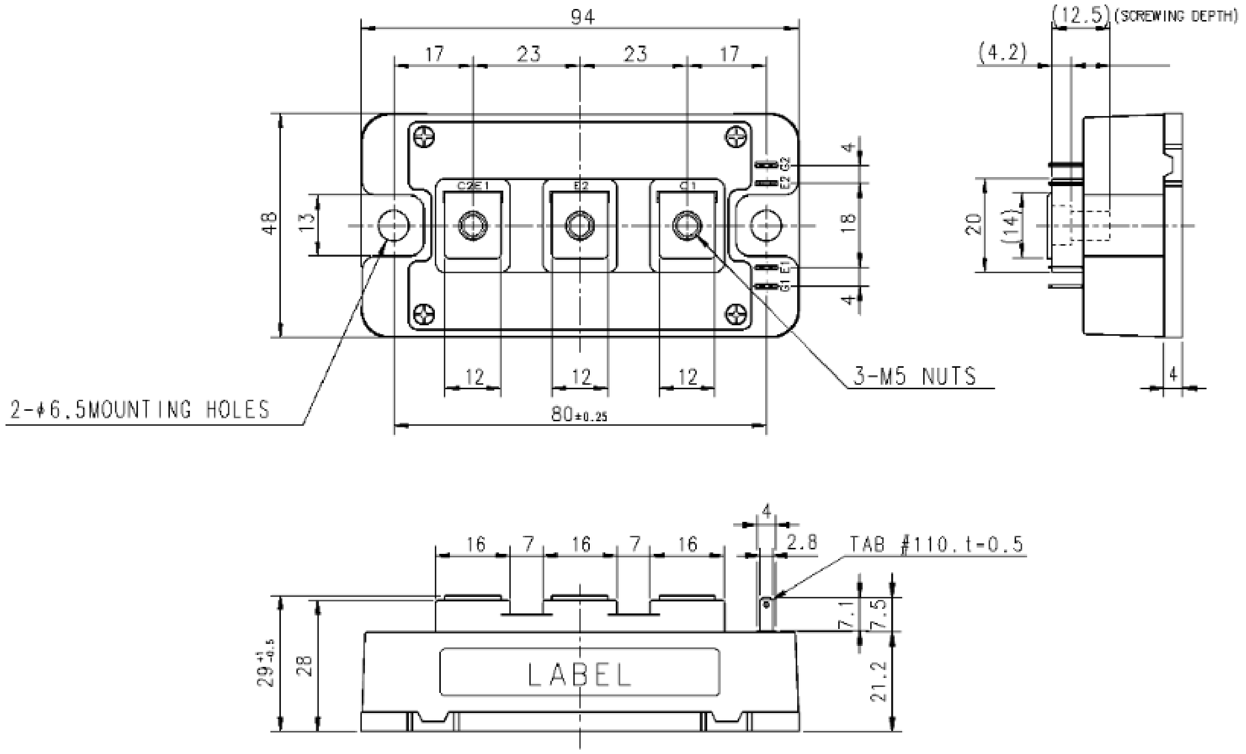
APPLICATION

Medical equipment, Welder, Power supply, etc.

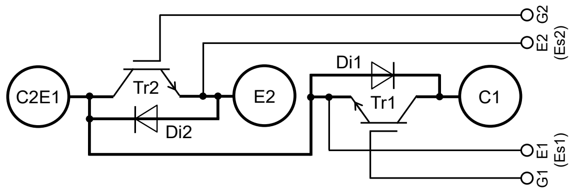
OPTION (Below options are available.)

●V_{CEsat} selection for parallel connection

OUTLINE DRAWING & INTERNAL CONNECTION **Dimension in mm**



INTERNAL CONNECTION



Tolerance otherwise specified

Division of Dimension			Tolerance
0.5	to	3	±0.2
over 3	to	6	±0.3
over 6	to	30	±0.5
over 30	to	120	±0.8
over 120	to	400	±1.2

JIS B 0405 c

MAXIMUM RATINGS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions	Rating	Unit
V_{CES}	Collector-emitter voltage	G-E short-circuited	1200	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
I_C	Collector current	DC, T_C=25 °C (Note2, 4)	200	A
I_{CRM}		Pulse, Repetitive (Note3)	400	
P_{tot}	Total power dissipation	T_C=25 °C (Note2, 4)	970	W
I_E (Note1)	Emitter current	DC, T_C=25 °C (Note2)	200	A
I_{ERM} (Note1)		Pulse, Repetitive (Note3)	400	
V_{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T_{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note 8)	175	°C
T_{Cmax}	Maximum case temperature	(Note4, 8)	125	
T_{vjop}	Operating junction temperature	Continuous operation (under switching) (Note 8)	-40 ~ +150	°C
T_{stg}	Storage temperature	-	-40 ~ +125	

ELECTRICAL CHARACTERISTICS (T_{vj}=25 °C, unless otherwise specified)

Symbol	Item	Conditions		Limits			Unit
				Min.	Typ.	Max.	
I_{CES}	Collector-emitter cut-off current	V_{CE}=V_{CES}, G-E short-circuited	T_{vj}=25 °C	-	-	1.0	mA
			T_{vj}=150 °C			50.0	
I_{GES}	Gate-emitter leakage current	V_{GE}=V_{GES}, C-E short-circuited		-	-	0.5	μA
$V_{GE(th)}$	Gate-emitter threshold voltage	I_C=20 mA, V_{CE}=10 V		5.40	6.00	6.60	V
V_{CEsat} (Terminal)	Collector-emitter saturation voltage	I_C=200 A, V_{GE}=15 V, Refer to the figure of test circuit (Note5)	T_{vj}=25 °C	-	4.45	5.15	V
			T_{vj}=125 °C	-	4.55	-	
			T_{vj}=150 °C	-	4.45	-	
V_{CEsat} (Chip)		I_C=200 A, V_{GE}=15 V, (Note5)	T_{vj}=25 °C	-	4.35	5.05	V
			T_{vj}=125 °C	-	4.45	-	
			T_{vj}=150 °C	-	4.35	-	
C_{ies}	Input capacitance	V_{CE}=10 V, G-E short-circuited		-	-	30.0	nF
C_{oes}	Output capacitance			-	-	2.5	
C_{res}	Reverse transfer capacitance			-	-	0.5	
Q_G	Gate charge	V_{CC}=600 V, I_C=200 A, V_{GE}=15 V		-	0.5	-	μC
$t_{d(on)}$	Turn-on delay time	V_{CC}=600 V, I_C=200 A, V_{GE}=±15 V, R_G=0 Ω, Inductive load		-	-	300	ns
t_r	Rise time			-	-	80	
$t_{d(off)}$	Turn-off delay time			-	-	500	
t_f	Fall time			-	-	100	
V_{EC} (Note.1) (Terminal)	Emitter-collector voltage	I_E=200 A, G-E short-circuited, Refer to the figure of test circuit (Note5)	T_{vj}=25 °C	-	2.45	2.85	V
			T_{vj}=125 °C	-	2.60	-	
			T_{vj}=150 °C	-	2.55	-	
V_{EC} (Note.1) (Chip)		I_E=200 A, G-E short-circuited, (Note5)	T_{vj}=25 °C	-	2.35	2.75	V
			T_{vj}=125 °C	-	2.50	-	
			T_{vj}=150 °C	-	2.45	-	
t_{rr} (Note1)	Reverse recovery time	V_{CC}=600 V, I_E=200 A, V_{GE}=±15 V, R_G=0 Ω, Inductive load		-	-	250	ns
Q_{rr} (Note1)	Reverse recovery charge			-	13	-	μC
E_{on}	Turn-on switching energy per pulse	V_{CC}=600 V, I_C=I_E=200 A, V_{GE}=±15 V, R_G=0 Ω, T_{vj}=150 °C, Inductive load		-	5.0	-	mJ
E_{off}	Turn-off switching energy per pulse			-	10.0	-	
E_{rr} (Note1)	Reverse recovery energy per pulse			-	10.0	-	mJ
$R_{CC'+EE'}$	Internal lead resistance	Main terminals-chip, per switch, T_C=25 °C (Note4)		-	0.4	-	mΩ
r_g	Internal gate resistance	Per switch		-	1.6	-	Ω

THERMAL RESISTANCE CHARACTERISTICS

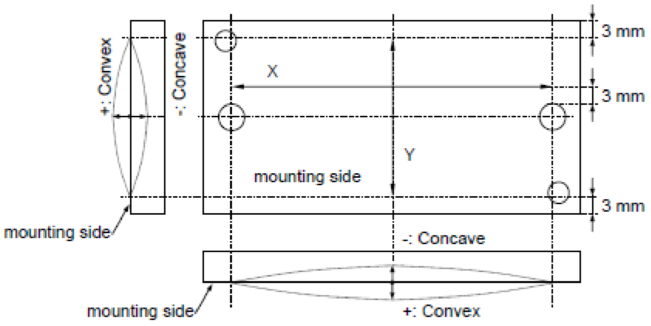
Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
$R_{th(j-c)Q}$	Thermal resistance	Junction to case, per Inverter IGBT (Note4)	-	-	154	K/kW
$R_{th(j-c)D}$		Junction to case, per Inverter FWD (Note4)	-	-	284	
$R_{th(c-s)}$	Contact thermal resistance	Case to heat sink, per 1 module, Thermal grease applied (Note4, 6, 8)	-	11	-	K/kW

MECHANICAL CHARACTERISTICS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
M_t	Mounting torque	Main terminals M 5 screw	2.5	3.0	3.5	N·m
M_s	Mounting torque	Mounting to heat sink M 6 screw	3.5	4.0	4.5	N·m
d_s	Creepage distance	Terminal to terminal	17.0	-	-	mm
		Terminal to base plate	28.5	-	-	
d_a	Clearance	Terminal to terminal	11.0	-	-	mm
		Terminal to base plate	25.6	-	-	
e_c	Flatness of base plate	On the centerline X ,Y (Note7)	-50	-	+100	μm
m	mass	-	-	310	-	g

*: This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU)2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).
2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed T_{vjmax} rating.
4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.
6. Typical value is measured by using thermally conductive grease of λ=0.9 W/(m·K)/$D_{(C-S)}$=50 μm.
7. The base plate (mounting side) flatness measurement point (X,Y) is as follows of the following figure.



8. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition ($T_{vj\,max}$, $T_{vj\,op}$, $T_{C\,max}$) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

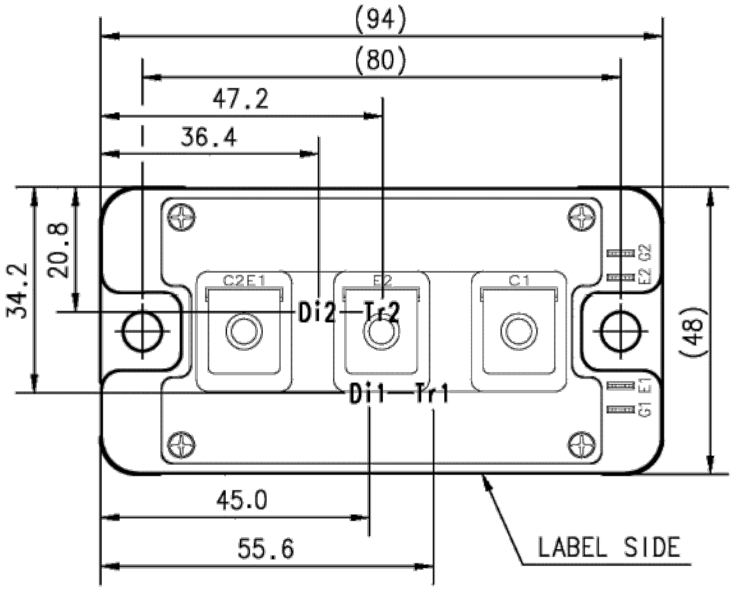
※ No short circuit capability is designed.

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions	Limits			Unit
			Min.	Typ.	Max.	
V_{CC}	(DC) Supply voltage	Applied across C1-E2 terminals	-	600	850	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	0	-	10	Ω
f_C	Switching frequency	V_{CC}=600 V, R_G=0 Ω, V_{GE}=±15 V,T_{vj}=150℃	-	-	60	kHz

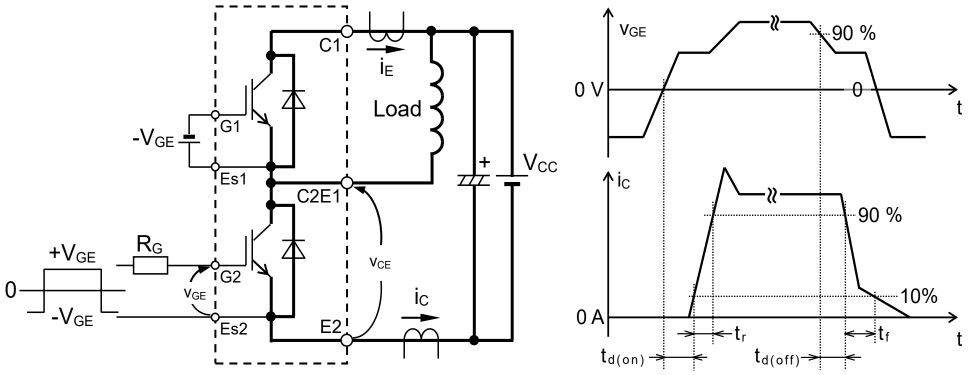
CHIP LOCATION (Top view)

Dimension in mm, tolerance: ±1 mm

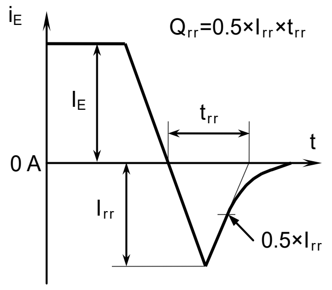


Tr1/Tr2: IGBT, Di1/Di2: FWD

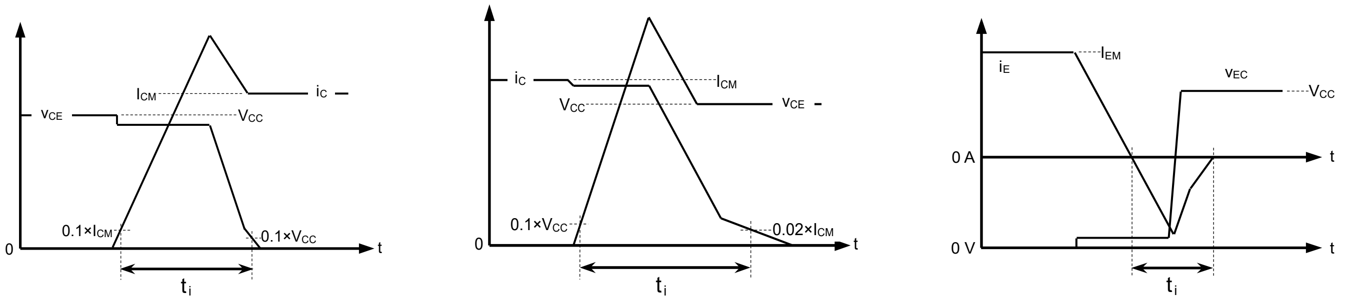
TEST CIRCUIT AND WAVEFORMS



Switching characteristics test circuit and waveforms



t_{rr}, Q_{rr} characteristics test waveform



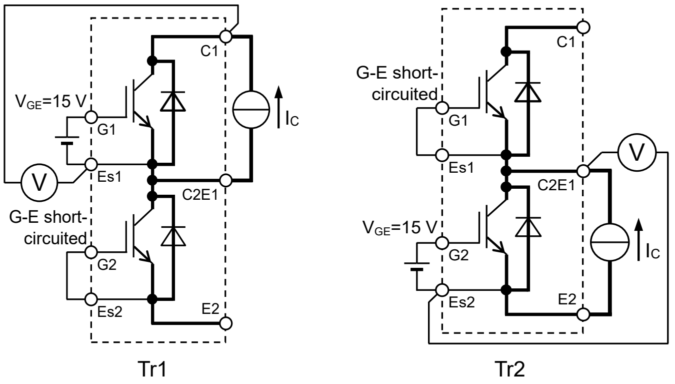
IGBT Turn-on switching energy

IGBT Turn-off switching energy

FWD Reverse recovery energy

Turn-on / Turn-off switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

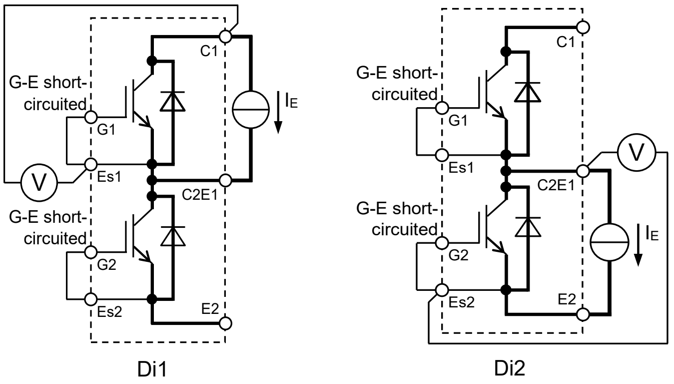
TEST CIRCUIT



Tr1

Tr2

V_{CEsat} characteristics test circuit



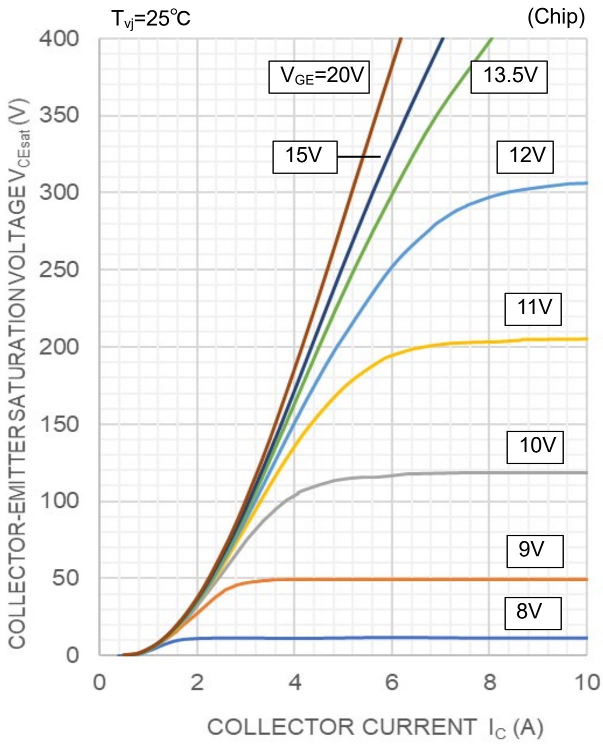
Di1

Di2

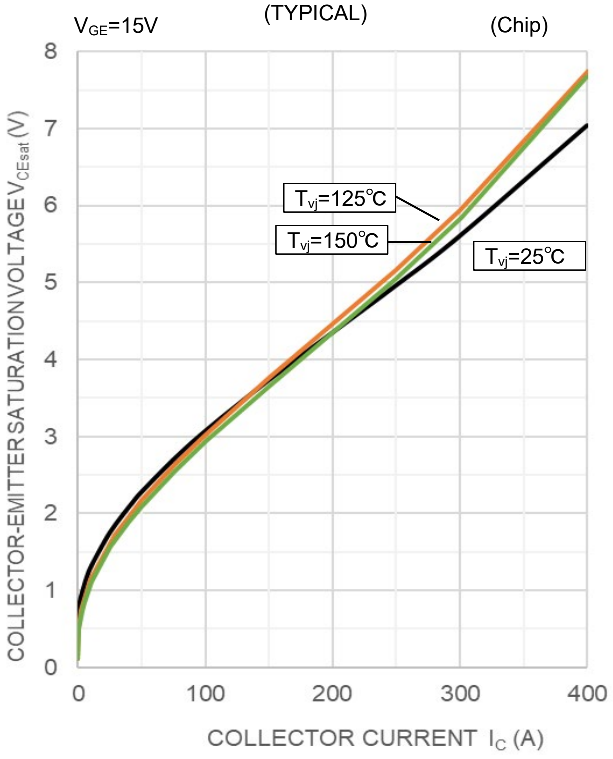
V_{EC} characteristics test circuit

PERFORMANCE CURVES

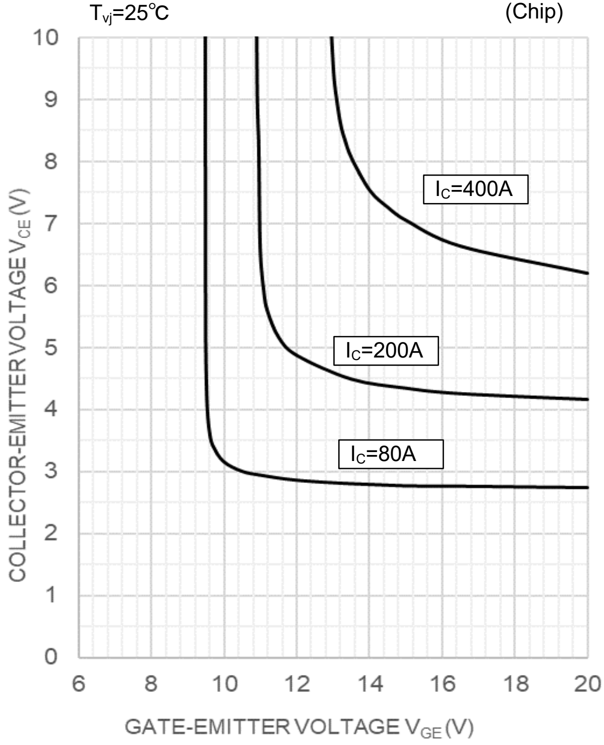
OUTPUT CHARACTERISTICS (TYPICAL)



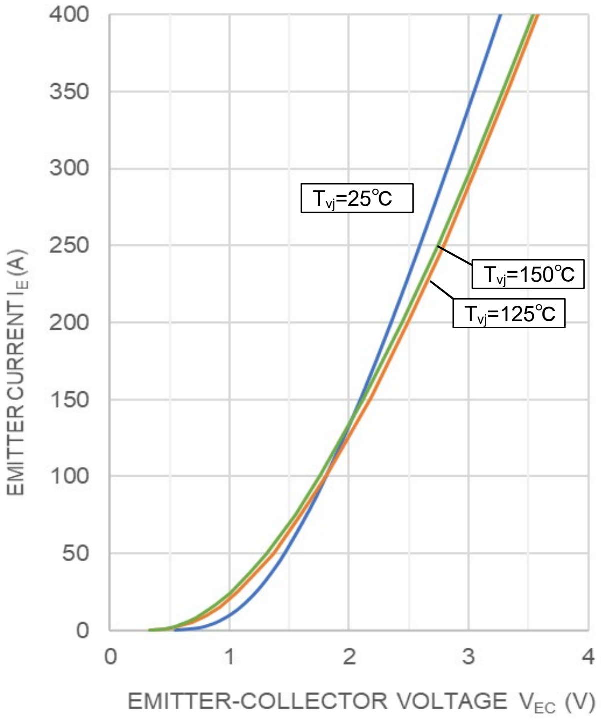
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGECHARACTERISTICS (TYPICAL)



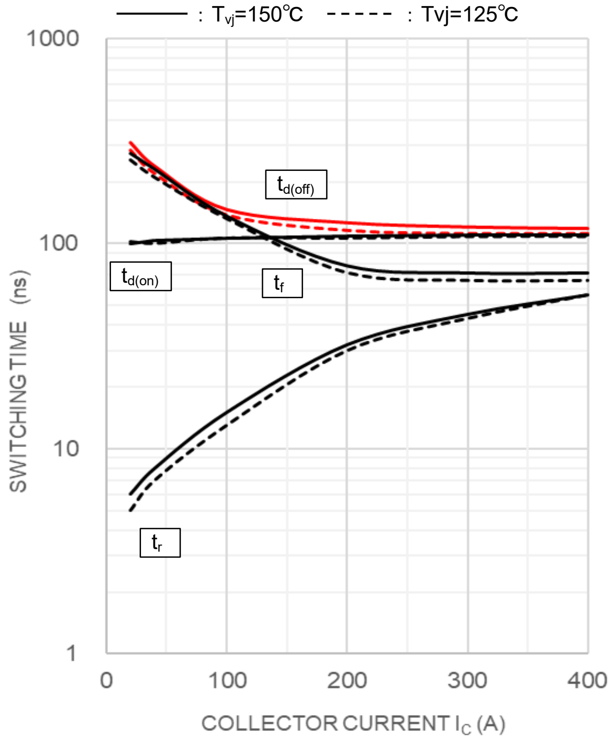
FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



PERFORMANCE CURVES

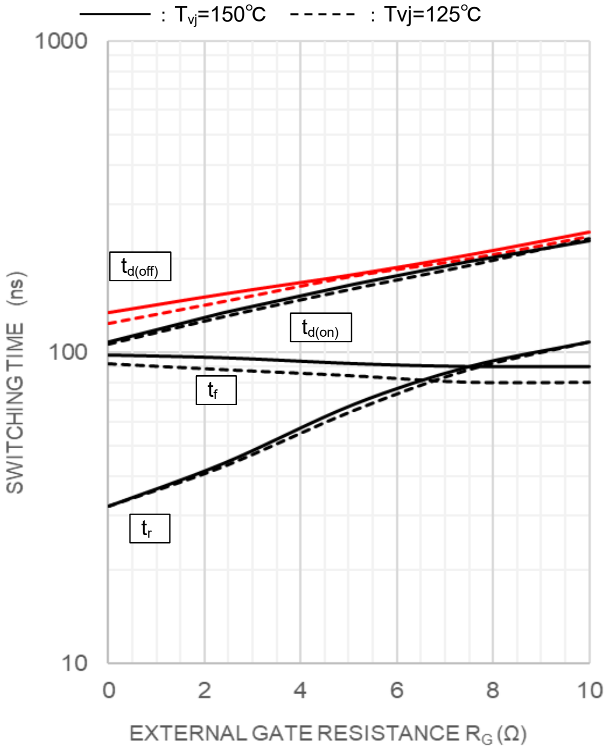
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600V, V_{GE}=±15V, R_G=0Ω, INDUCTIVE LOAD



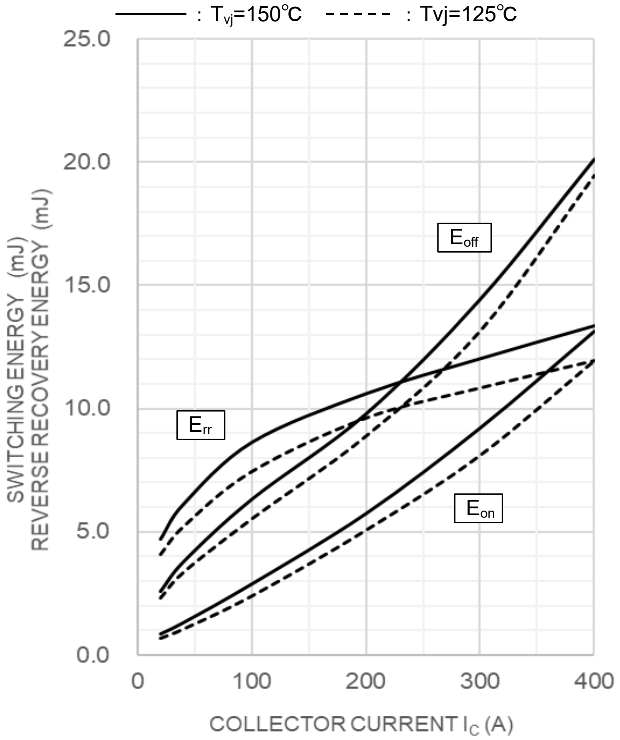
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600V, V_{GE}=±15V, I_C=200A, INDUCTIVE LOAD



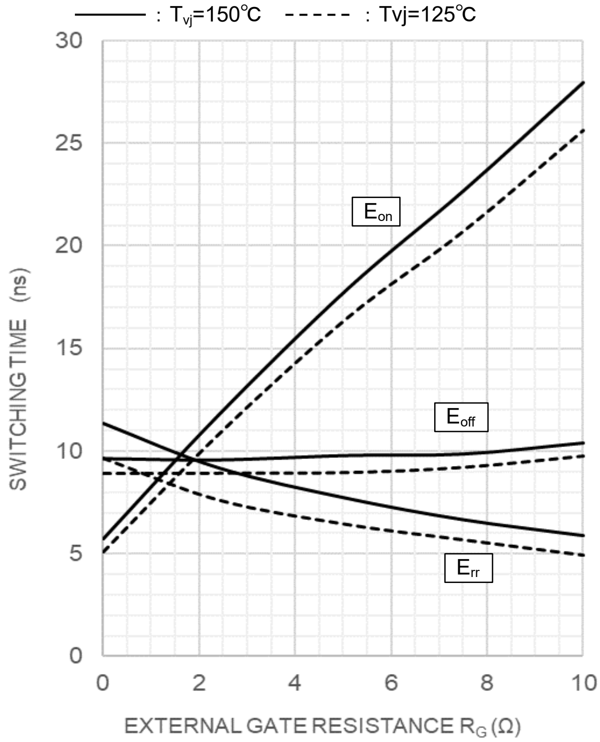
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

V_{CC}=600V, V_{GE}=±15V, R_G=0Ω, INDUCTIVE LOAD



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)

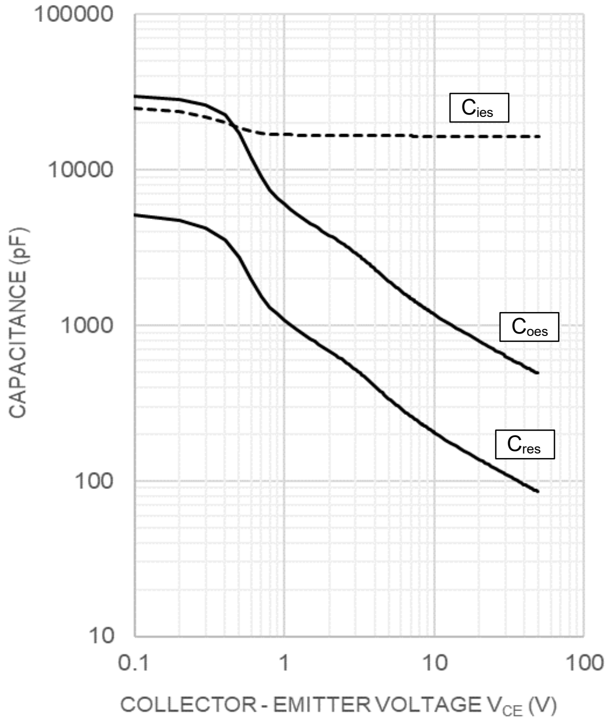
V_{CC}=600V, V_{GE}=±15V, I_C=200A, INDUCTIVE LOAD



PERFORMANCE CURVES

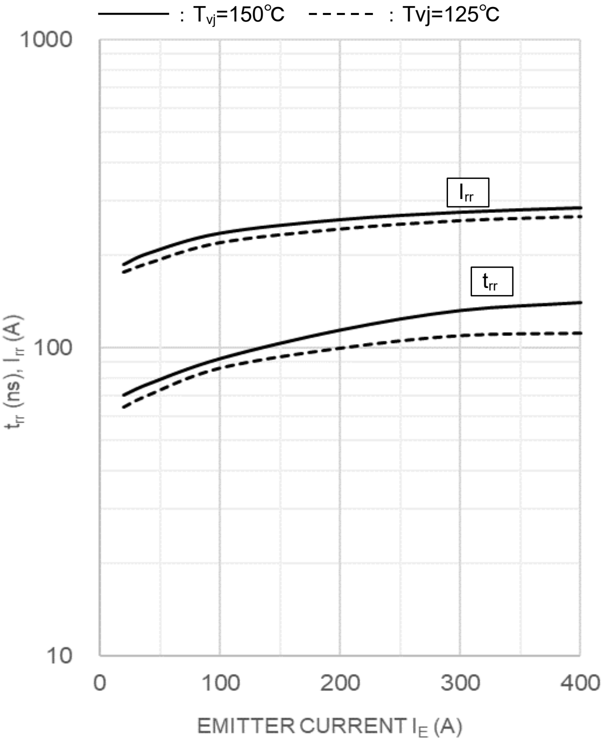
CAPACITANCE CHARACTERISTICS
(TYPICAL)

G-E short-circuited, T_{vj}=25℃



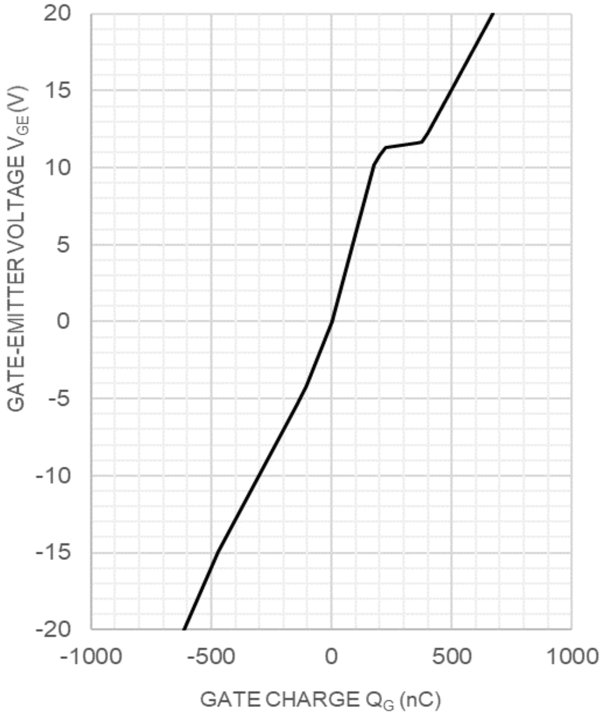
FREE WHEELING DIODE
REVERSE RECOVERY CHARACTERISTICS
(TYPICAL)

V_{CC}=600V, V_{GE}=±15V, R_G=0Ω, INDUCTIVE LOAD



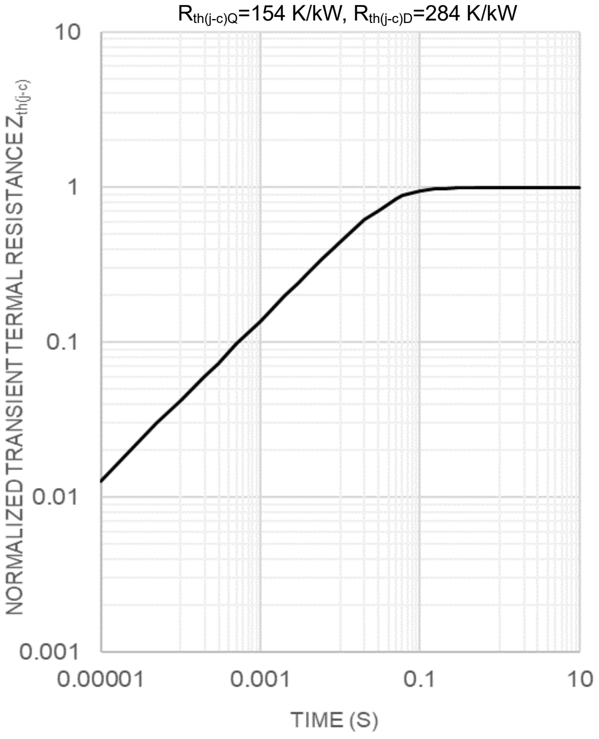
GATE CHARGE CHARACTERISTICS
(TYPICAL)

V_{CC}=600V, I_C=200A, T_{vj}=25℃



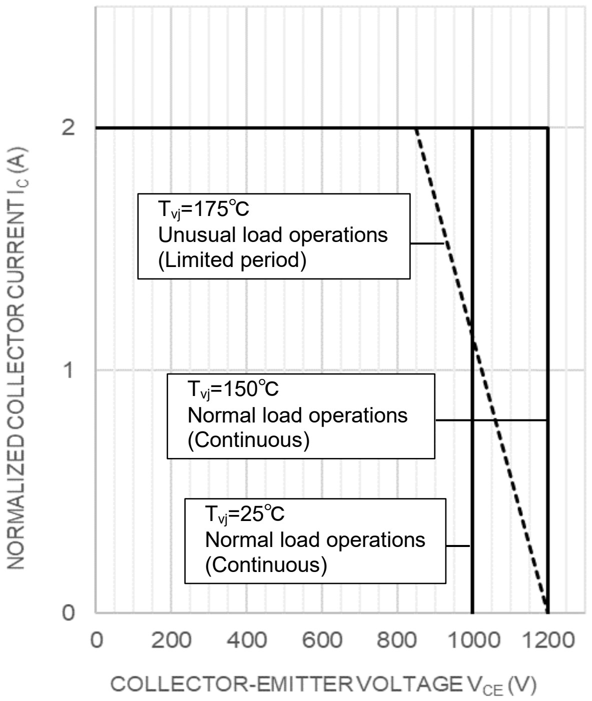
TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS
(MAXIMUM)

Single pulse, T_C=25℃
$R_{th(j-c)Q}$=154 K/kW, $R_{th(j-c)D}$=284 K/kW

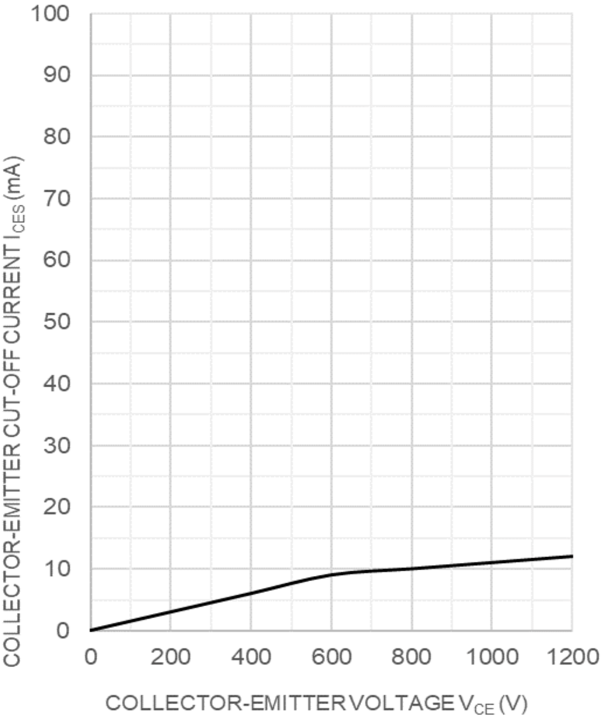


PERFORMANCE CURVES

TURN-OFF SWITCHING SAFE OPERATING AREA
(REVERSE BIAS SAFE OPERATING AREA)
(MAXIMUM)
V_{CC}=600V, I_C=200A, T_{vj}=25℃



COLLECTOR-EMITTER CUT-OFF CURRENT
CHARACTRISTICS
(TYPICAL)
T_{vj}=150℃, G-E short-circuited



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

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